

Logic Cube

PC-BASED LOGIC ANALYZER

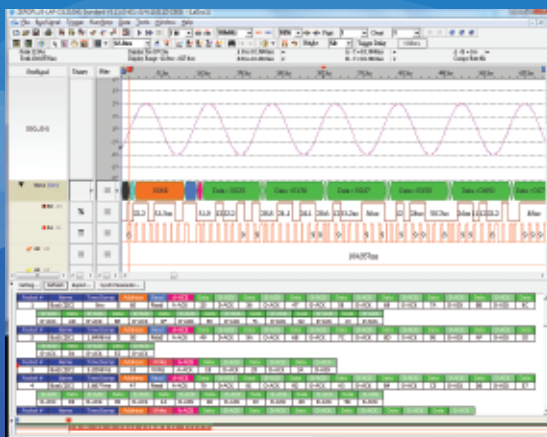
Buy now and get **110** protocol decoders for free!



More than **25,000** sold!

PC-Based UI: Set up and see the acquired data on a PC using the Logic Cube software; a straight-forward interface makes for quick acquisition setups and debugging.

Protocol Decoders: The Logic Cube comes with more than 110 protocol decoders spanning all major technologies and industries, including the popular I2C, UART, SPI, 1-WIRE, HDQ, CAN2.0B, I2S, PS/2, MICROWIRE etc. We also provide a free protocol decoder development kit.



Auxiliary Analysis Windows: Use the Memory Analyzer, Packet List and Statistics functions to quickly understand what's going on with your signals; where is the data written, what is the content of packet Y, how many highs does channel X have etc.

Data Compression: The Logic Cube incorporates a sophisticated compression algorithm that lets you multiply your effective memory to do longer acquisitions without losing any information.

Filter Signals: Economize memory further by setting filter conditions. Ex: Store data only when Signal 5 is high and Signal 12 is low.

Hardware Stacking: Link several Logic Cubes to increase both memory depth and channel count.

DSO Connection: The Logic Cube can be connected to DSOs so that both analog and digital signals can be captured and displayed.

| Model | | LAP-C(16032) | LAP-C(16064) | LAP-C(16128) | LAP-C(162000) | LAP-C(32128) | LAP-C(322000) |
|-----------------------------|--------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------|--------------|------------------|--------------------------------------------|--------------|---------------|
| Supported operating systems | | Win 7 and older (workaround for Win 8.1 and Win 10 exists) | | | | | |
| Acquisition Channels | | 16 | | | 32 | | |
| Data transfer | | USB 2.0 | | | | | |
| Sample Rate | Internal Clock (Timing Mode) | 100 Hz - 100 MHz | | 100 Hz - 200 MHz | | | |
| | External Clock (State Mode) | 75 MHz | | 100 MHz | | | |
| Memory | Total Memory | 512 kbits | 1 Mbits | 4 Mbits | 64 Mbits | 4 Mbits | 64 Mbits |
| | Memory per Channel | 32 kbits | 64 kbits | 128 kbits | 2 Mbits | 128 kbits | 2 Mbits |
| | Max compression ratio | 256 | | | | | |
| Threshold Voltage | Bandwidth | 75 MHz | | | | | |
| | Range | -6V to + 6V | | | | | |
| | Precision | ±0.1V | | | | | |
| Trigger | Trigger Channels | 16 | | | 32 | | |
| | Trigger Events | State / Edge / Pattern | | | | | |
| | Pulse-width Trigger | PW trigger is included in the 322000 model and can be purchased separately for the other models | | | | | |
| | Pre/Post Triggering | Store both pre-trigger data and post-trigger data (choose from 0-100% pre-trigger data) | | | | | |
| | Trigger Delay | YES; based on memory or time | | | | | |
| | Trigger Sequence Levels | YES | | | | | |
| | Trigger Out | YES; send a trigger signal that sets off another instrument | | | | | |
| | Trigger Pass Counter | 65,535 | | | | | |
| Software Functions | UI Languages | English, Chinese (Simplified), Chinese (Traditional) | | | | | |
| | Zooming and panning | 2 cursor modes | | | | | |
| | Waveform and UI customization | Modify the appearance of channels, menus, traces, windows etc | | | | | |
| | State list and Waveform view | Present the samples as a list of 0s and 1s or as traces | | | | | |
| | DSO Connection | Connect to and import signals from DSOs | | | | | |
| | Data Contrast | Compare 2 files to quickly see where and how they differ; included in the 162000 and 322000 models | | | | | |
| | Navigator | Quickly get an overview and navigate to distant parts of the waveform | | | | | |
| | Memory View | See what the memory looks like; what is read/written to which address | | | | | |
| | Packet List | Breakdown of all packet details into convenient list form | | | | | |
| | Statistics | Table view of number of periods, periods that satisfy conditions etc | | | | | |
| | Protocol Decoders | Decode more than 110 protocols for free! | | | | | |
| Double Mode | Double the available memory per channel when only using half of the channels; available for 32 channel models only | | | | | | |
| Electrical Properties | Phase Errors | < 1.5 ns | | | | | |
| | Source | DC Connection | | | | | |
| | Power | 5V DC, 500 mA (1W standby) | | | | | |
| | Maximum Input Voltage | ±30V | | | | | |
| | Impedance | 500kΩ/10pF | | | | | |
| Certifications | | FCC / CE / WEEE / RoHS | | | | | |
| Dimension | | 125 x 92 x 25 (mm) | | | | | |
| Standard Accessories | Flying Lead Test Cables | 8-pin x2 / 2-pin x1 / 1-pin x1 | | | 16-pin x1 / 8-pin x2 / 2-pin x1 / 1-pin x1 | | |
| | Clips-on Probe Connectors | 2 | 20 | | | 36 | |
| | USB Cable for connection to PC | YES | | | | | |
| | Software and driver CD | YES | | | | | |
| | Printed Installation Guide | NO | YES | | | | |
| Carrying Bag | NO | YES | | | | | |

Accessories

Standard Accessories

Pulse-width Trigger Module (Option)

Add a Pulse-width trigger module to give the Logic Cube pulse-width triggering capability.

Contents

- Pulse-width Trigger Module
- 7-pins Test cable
- 2-pins Test cable
- User Guide

5.4 x 4.5 x 1.8 cm / 50 gram

I2C / SPI Control Center (Option)

The I2C-SPI control center is a separate module used to generate custom signals based on the I2C or SPI protocols. In just a few clicks users can set up a communication with another I2C or SPI device.

Included: GPIO Mode, SPI Mode, I2C Mode, I2C Monitor...



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