

“Image Encode” – Emulating trouble digital signal

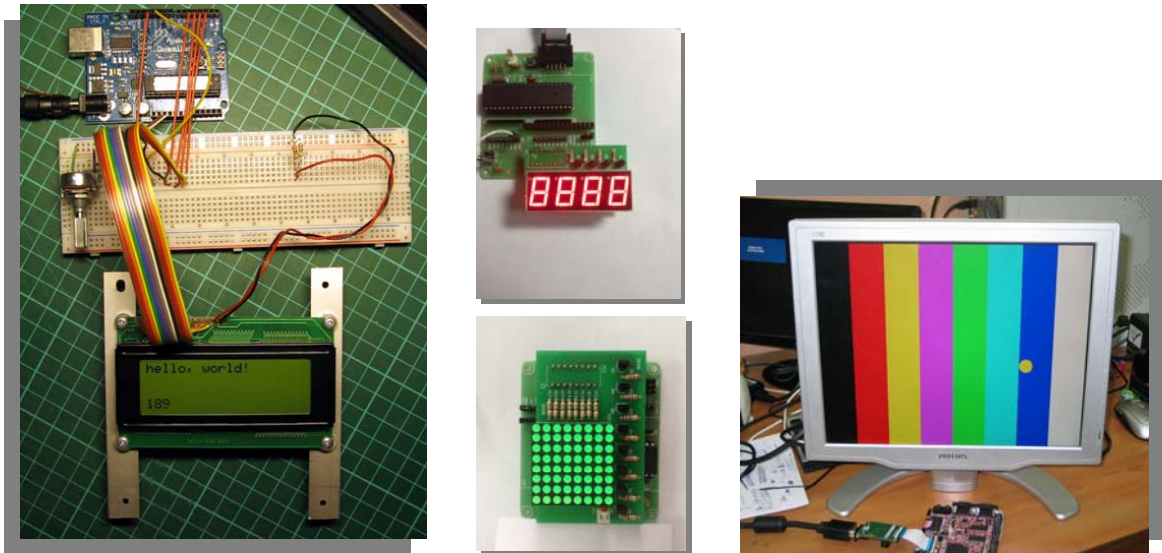
(Taiwan Patent -100130937 · China Patent - 2011110271756.6)

In product development projects, it usually happen that hardware & software engineers need to co-work to debug failure issues. Somehow they will spend more time to figure out whether the bug are on hardware circuit or on soft/firmware. Worse thing then wasting time is upset arguments that hurts teamwork ship ! ZEROPLUS measurement tool give you good solution ~

ZEROPLUS Logic Analyzers provide strong “Image Encode” function that aim for several protocols such as - (CMOS Image Sensor) · LCD(12860 & 1602) · 7-Segment · LED Pitch Array · [DMX512](#). Our patented tech let those digital signals encode to it's displayed 1 or multi-frame pictures. Thus, don't need to wait hardware support specific evaluation board device and no need to analysis huge raw waveform data!

Below shown the demo diagrams~

Before ~ wasting time and energy & hard to find fail signals.



After ~ easily find wrong displayed signals directly on logic analyzer software (emulation) !



“Image Encode” turns the numeric data to image display, debug work tend to be intuitional and comprehensive !