

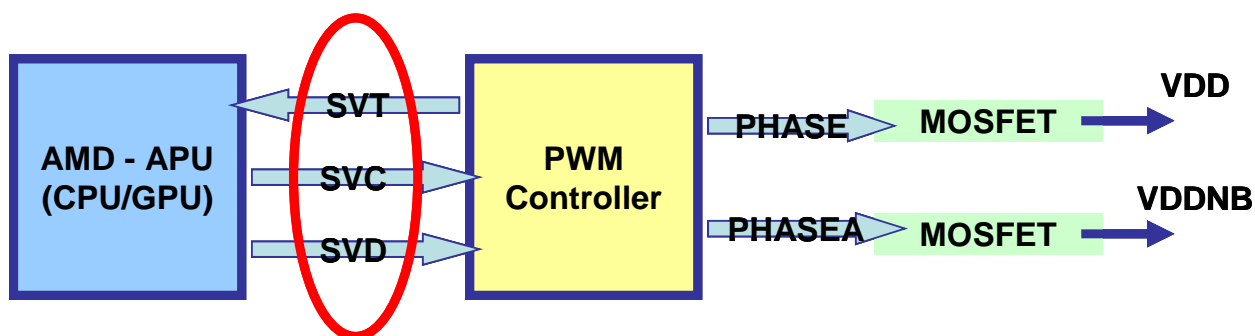
Powerful power control interface - AMD SVI2

Improve the stability of power control has always been thought to be pursued by every system engineers. A bad product in the case of unstable power control operation, of course, will often result in some dysfunction!

ZEROPLUS hear the voices of the engineers and launched a new [“AMD_SVI2 bus protocol analysis module”](#) specifically for those who required power control measurement of PWM controller IC for AMD CPU power supply system. ZeroPlus provided a solution that can quickly capture, decode and analysis signals. How do we make engineers can easily get instant analysis and cross comparison validation and other effects it? Let's see more...

SVI2 – full in “Serial VID (Voltage Identification) Interface 2.0”, is dedicated by AMD to monitor CPU / Northbridge (NB) voltage protocol, commonly found in power supply control IC for the CPU and the NB voltage control, so you can view APU load conditions , and then adjust the voltage value, so a more stable power supply more efficient. Simple structure as shown below.

AMD SVI2 Protocol



Improve the stability of power control in order to effectively reduce the occurrence rate of customer complaints and failure product. So how to control the voltage stability, which is one of the necessary skills required for every system engineer. Use ZeroPlus proposed AMD SVI2 measurement solutions, powerful software functionality directly decode the captured waveform is displayed as voltage table, allows engineers to control the real-time status of voltage, then do analysis and fine-tuning. Reduce trivial to verify and compare works, you can dramatically shorten development time!

It also provides the SVT (directional communication based on packet group include: Core, NB, TFN), allows engineers to more quickly control between the CPU and PWM Controller operational status. Unique search and statistical functions let “Bug” no longer hiding!

