

Protocol Simulator Board 1



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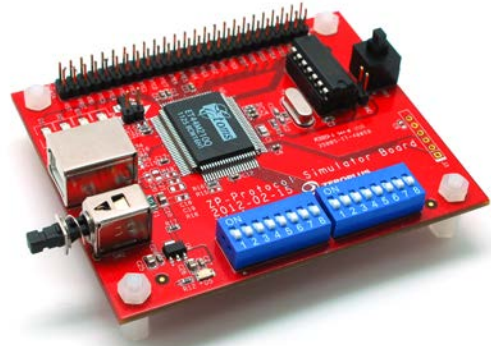
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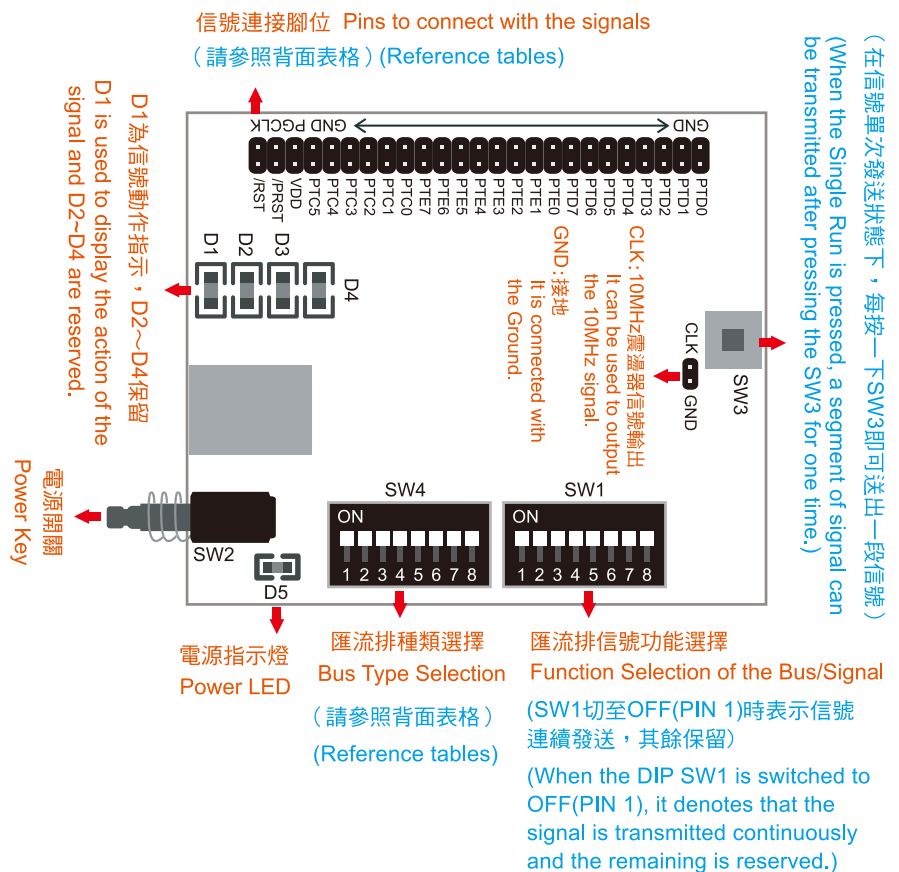
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Brief Introduction

- size: 7 cm x 9.7 cm x 2.5cm
- 1-WIRE, 3-WIRE, 7-SEGMENT LED, AC97, ARITHMETICAL LOGIC, CCIR656, DIGITAL LOGIC, DMX512, DSA Interface, FlexRay2.1A, HDQ, IRDA, I2C, I2S, JK FLIP-FLOP, JTAG2.0, LCD12864, LPC-SERIRQ, LCD1602, LIN 2.1, Low Pin Count, MCU-51 DECODE, ModBus, MANCHESTER, MICROWIRE, MII, MILLER, NEC PD6122, PCM, PECL, PM BUS1.1, PSB Interface, PS/2, SLE4442, SM BUS2.0, ST7669, ST BUS, SD2.0/SDIO, S/PDIF, SPI, SSI Interface, UART(RS-232C/422/485), UP COUNTER, USB1.1, CAN 2.0B, DIGRF
- This function can simulate the bus signal outputting (CMD, CLK, DATA), and decode the signal with the LA. Now nearly 100 buses are supported and the number still grows. The LA can quickly decode the signal that contains supported protocol analyzer packet format. For example, the signal sent by I2C Start, Address, Read, Write, Data, A-ACK, D-ACK, Stop, etc.



NO.	Bus Name	Switch Mode of SW4 (Dip Switch)	Pin Description
0	Up Counter		Up to 0X00-0XFF = PTD0-PTD7
1	I2C		SDA = PTD0 SCL = PTD1
2	SPI		SCK = PTD0 CS = PTD1 DATA0 = PTD2
3	UART (RS-232C/422/485)		TX = PTD0
4	1-Wire		ONE WIRE I/O = PTD0
5	HDQ		HDQ = PTD0
6	CAN2.0B		CAN_L = PTD0 CAN_H = PTD1
7	PS/2		CLOCK = PTD0 DATA = PTD1
8	I2S		LRCK = PTD0 SCLK = PTD1 SDA = PTD2
9	USB1.1		D+ = PTD0 D- = PTD1
10	Low Pin Count		LCLK = PTD0 LFRAME = PTD1 LAD[3] = PTD2 LAD[2] = PTD3 LAD[1] = PTD4 LAD[0] = PTD5
11	SSI Interface		SLK = PTD0 FS = PTD1 RD/TD = PTD2
12	MICROWIRE		SLK = PTD0 SS = PTD1 DATA = PTD2
13	LIN 2.1		LIN = PTD0
14	MANCHESTER		MANCHESTER = PTD0
15	MILLER		MILLER = PTD0
16	S/PDIF		S/PDIF = PTD0
17	SD2.0/SDIO		CLK = PTD0 CMD = PTD1 DATA[3] = PTD4 DATA[2] = PTD5 DATA[1] = PTD6 DATA[0] = PTD7

NO.	Bus Name	Switch Mode of SW4 (Dip Switch)	Pin Description
18	DIGITAL LOGIC		NOT In = PTD0 Out = PTD1 AND In1 = PTD2 In2 = PTD3 Out = PTD4 OR In1 = PTD5 In2 = PTD6 Out = PTD7 NAND In1 = PTE0 In2 = PTE1 Out = PTE2 NOR In1 = PTE3 In2 = PTE4 Out = PTE5 XOR In1 = PTE6 In2 = PTE7 Out = PTE0 XNOR In1 = PTC1 In2 = PTC2 Out = PTC3
19	ARITHMETICAL LOGIC		Multipier Multiplicand=PTD0-PTD3 Multiplier=PTD4-PTD7 Product=PTE0-PTE7 Divider Divided=PTD0-PTD3 Divisor=PTD4-PTD7 Quotient=PTE0-PTE3 Remainder=PTE4-PTE7 Subtractor Minued=PTD0-PTD3 Subtrahend=PTD4-PTD7 Difference=PTE0-PTE3 Borrow Output=PTE4 Borrow Input=PTE5
20	MII		MII: TX/RX_CLK = PTD0 TX_EN/RX_DV = PTD1 TX_RX_ER = PTD2 TX_COL = PTD3 TX_RX_D3 = PTD4 TX_RX_D2 = PTD5 TX_RX_D1 = PTD6 TX_RX_D0 = PTD7 SM: MDC = PTE0 MDIO = PTE1
21	LCD1602		DB[0] = PTD0 DB[1] = PTD1 DB[2] = PTD2 DB[3] = PTD3 DB[4] = PTD4 DB[5] = PTD5 DB[6] = PTD6 DB[7] = PTD7 RS = PTE0 R/W = PTE1 E = PTE2
22	ModBus		MODBUS = PTD0
23	7-SEGMENT LED		A = PTD0 B = PTD1 C = PTD2 D = PTD3 E = PTD4 F = PTD5 G = PTD6 COM = PTD7
24	ST Bus		FR = PTD0 SDA = PTD1 CLK = PTD2
25	IRDA		IRDA_SIR = PTD0 ASK_IR = PTD1 IRDA_HDLC = PTD2 IRDA_FIR = PTD3
26	DMX512		DMX_D+ = PTD0 DMX_D- = PTD1
27	FLEXRAY 2.1A		TXD = PTD0 TXEN = PTD1
28	LPC-SERIRQ		LCLK = PTD0 SERIRO = PTD1
29	JTAG2.0		TCK = PTD0 TMS = PTD1 TDI/O = PTD2 TREST = PTD3
30	DSA Interface		DATA = PTD0 STB = PTD1 ACK = PTD2

NO.	Bus Name	Switch Mode of SW4 (Dip Switch)	Pin Description
31	ST7669		3-Wire Mode : /CS = PTD0 SCL = PTD1 SI = PTD2 4-Wire Mode : /CS = PTD4 A0 = PTD5 SCL = PTD6 SI = PTD7
32	LCD12864		RS/CS = PTD0 WR/SID = PTD1 E/SCLK = PTD2 DB[0] = PTD3 DB[1] = PTD4 DB[2] = PTD5 DB[3] = PTD6 DB[4] = PTD7 DB[5] = PTE0 DB[6] = PTE1 DB[7] = PTE2
33	PCM		SCLK = PTD0 FS = PTD1 DT = PTD2 DR = PTD3
34	MCU-51 DECODE		P0.0-P0.7 = PTD0-PTD7 P2.0-P2.7 = PTE0-PTE7 ALE = PTC0 PSEN = PTC1
35	NEC_PD6122		carrier wave mode DATA = PTD0 no carrier wave mode DATA = PTD1
36	PM Bus 1.1		CLK = PTD0 DATA = PTD1
37	PSB Interface		SD0 = PTD0
38	SM Bus 2.0		SMBLCK = PTD0 SMBDAT = PTD1
39	DIGRF		CTRL MODE CTRLCLK = PTD0 CTRLCLK = PTD1 CTRLDATA = PTD2 RXTX MODE RXTX_EN = PTD3 SYCLK = PTD4 RXTX_DATA = PTD5
40	AC97		SYNC = PTD0 BITCLK = PTD1 SDI/SDO = PTD2 RESET = PTD3
41	SLE4442		CLK = PTD0 I/O = PTD1 RST = PTD2
42	CCIR656		DB0 = PTD0 DB1 = PTD1 DB2 = PTD2 DB3 = PTD3 DB4 = PTD4 DB5 = PTD5 DB6 = PTD6 DB7 = PTD7 CLK = PTE0 Decimal[1] = PTE1 Decimal[0] = PTE2
43	PECI		PECI = PTD0
44	3-WIRE		CS = PTD0 SCLK = PTD1 I/O = PTD2
45	J-K FLIP-FLOP		CP = PTD0 CLR = PTD1 PR = PTD2 J/ \bar{J} = PTD3 K/ \bar{K} = PTD4 Q = PTD5 \bar{Q} = PTD6