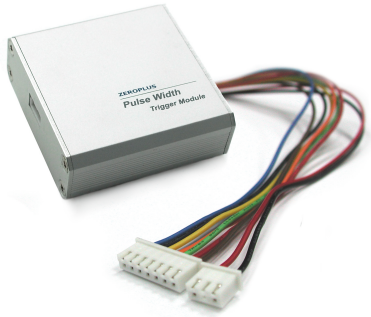
Pulse Width Trigger Module



Brief Introduction

The additional pulse width trigger module could upgrade logic analyzer pulse width trigger capability.

Accessories

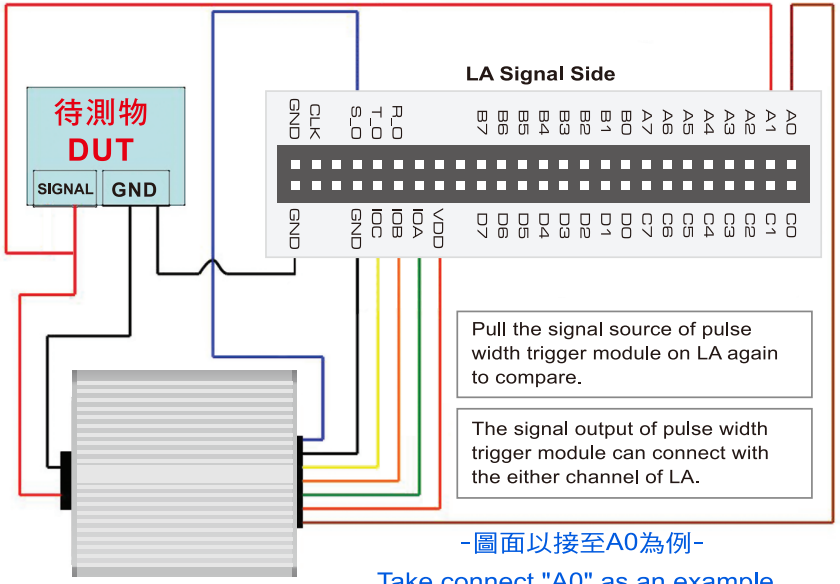
Pulse Width Trigger Module *1
test Cable(7pin) *1
test Cable(2pin) *1
User Guide *1

Specification

* Power: Supplied by LA (3.3V)
* Interface: Synchronously transfer the signal through five pins (IOA, IOB, IOC, S_O, OSTM).
* Input Frequency Range:
  12 Hz (83.88608ms) ~ 25M Hz (40ns)
* Input Range: 0 ~ 3.3V
* output DC: 3.6V (Max)
* Size: 5.4 cm x 4.5 cm x 1.8 cm
* Weight: 5g

孪龍邏輯分析儀軟體V3.03.05以上版本適用
Suit For V3.03.05 Version

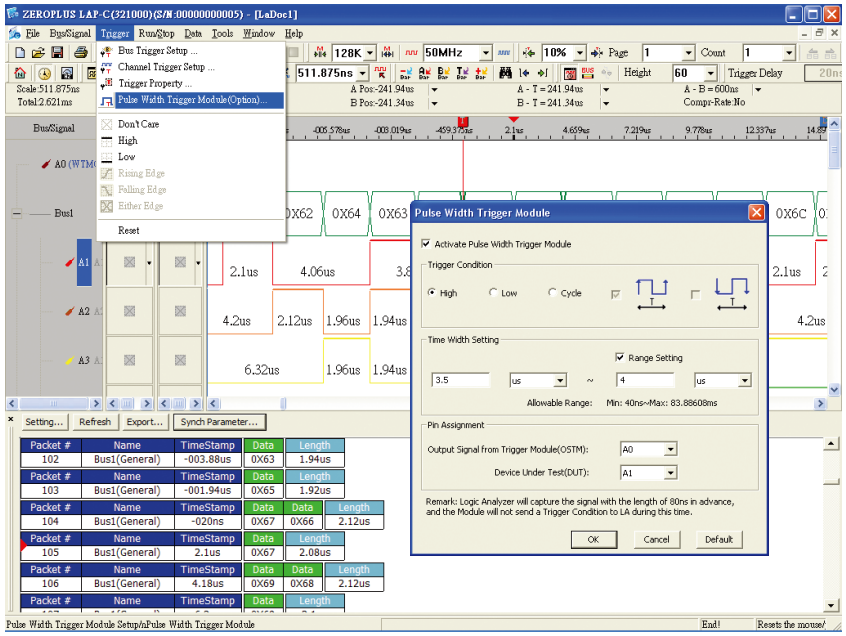
■ 硬體接線圖 Hardware Wiring Diagram



（ 機身背面 REAR ）

-圖面以接至A0為例-
Take connect "A0" as an example.

■ 軟體介面設定 Software Interface Setting



Features:

The additional pulse width trigger module could upgrade logic analyzer pulse width trigger capability.

In practice, if a project development needs to control the fade-in or fade-out of lighting in a PWM-like way, then engineers are in a great need of the pulse width trigger module to check the waveform width is in the normal range or not? Whether it is stable? If it is out of the predicted range, the LA will immediately display the waveform status of the trigger position, which helps engineers observe and debug the problems, so the developing time is shortened and the work efficiency is improved.

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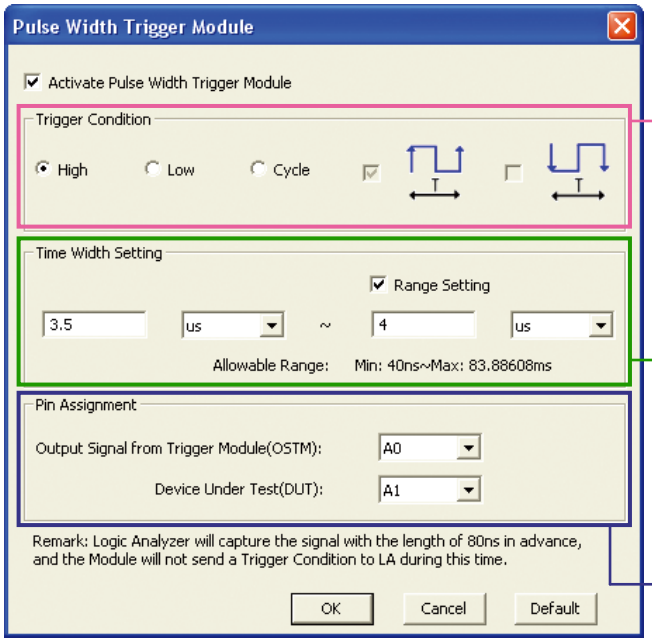
■ 操作步驟 Operating Steps



STEP 1：觸發條件 Trigger Condition

設定使用脈波寬度觸發時的觸發波形狀態，可設定狀態共有
高準位：以高準位波形寬度進行觸發設定。
低準位：以低準位波形寬度進行觸發設定。
完整週期：以取樣波形中的完整波形週期進行觸發設定。

Set the waveform status for the pulse width trigger:
High Level: Trigger by the high-level waveform width.
Low Level: Trigger by the low-level waveform width.
Complete Period: Trigger by the complete waveform period in the waveform to be sampled.

STEP 2：時間寬度設定 Time Width Setting

設定觸發條件中的時間資訊，滿足所設置的時間長度時即可達成觸發條件。若勾選範圍設定，則可用區間時間做為觸發條件設定

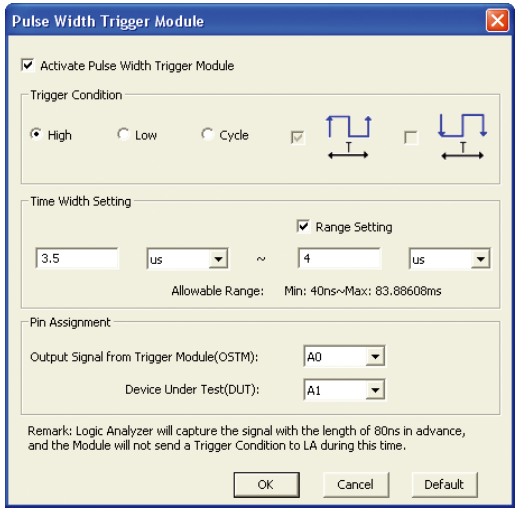
Set the time width in the trigger condition, the trigger will happen if that time width is satisfied.
If the “Range Setting” is selected, time width range can be set as the trigger condition.

STEP 3：通道設定 Pin Assignment

設定模組連接通道位置，共區分為
觸發模組輸出信號(OSTM)
待測信號(DUT)

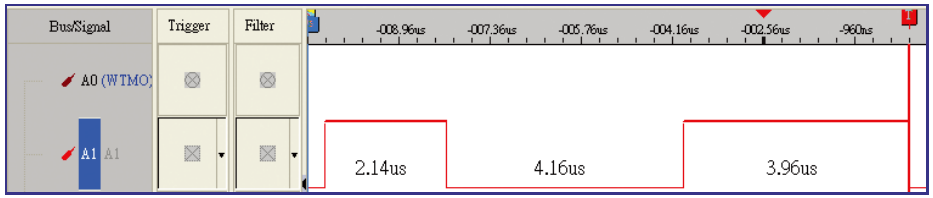
Set the pin assignment, including:
Output Signal Trigger Module (OSTM)
Signal Under Test (DUT)

■ 範例說明 Example



◀ 設定觸發條件為高準位，時間寬度設定已勾選範圍設定，時間參數為 3.5us ~ 4us

Set high level and time width range of 3.5us ~ 4us as the trigger condition.



◀ A0為脈波寬度觸發模組的Output，A1是原本訊號源。

AO is the output of pulse width trigger module,
A1 is the original signal channel.